## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned Examiner: To Be Assigned

In Re PATENT APPLICATION Of:

Applicants	:	Yoshinori SHIZUNO	)
Serial No.	:	To Be Assigned	)
Filed	:	October 31, 2003	) INFORMATION ) DISCLOSURE
For	:	SEMICONDUCTOR DEVICE WITH IMPROVED DESIGN FREEDOM OF EXTERNAL TERMINAL	) STATEMENT ) )
Attorney Ref.	:	OHG 140	) October 31, 2003

Commissioner for Patents P. O. Box 1450 Alexandria VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached are copies of three Japanese patent publications. Any relevance of the Japanese publications can be gleaned from the attached English-language Abstracts and from pages 2 and 3 of the present application, where two of these documents are discussed. The publications are listed on the attached Form PTO-1449. Also listed on the Form PTO-1449 for consideration is one U.S. patent. Any relevance of this U.S. patent is self-evident.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the documents submitted. Consideration of the submitted documents is respectfully requested.

Respectfully submitted

October 31, 2003

Date

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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				Atty Docket	Application No.  To Be Assigned					
				OHG 140						
				Applicant						
				Yoshinori SI	HIZUNO					
					Filing Date October 31, 2003			Group Unit  To Be Assigned		
			U.S.	PATENT DOCUM	IENTS					
Examiner Initial		Document Number	Date	Name		Class	Sub- Class	Filing Date		
	AA	6,455,920	6,455,920 09/24/02 Fukasawa et al.							
	AB									
	AC									
	AD									
	AE	W			,					
	AF									
	AG		<u></u>							
	AH									
		<u></u>	FOREIG	GN PATENT DOC	UMENTS					
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	AI	2000277682 A	10/06/00	Japan				Abstract		
	AJ	2000208556 A	07/28/00	Japan				Abstract		
	AK	11251493 A	09/17/99	Japan				Abstract		
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